券代B芯科技有限公司

EPORT Edge Port Module Block

Summary

The EPORT has eight external interrupt pins. Each pin may be configured individually as a low level-sensitive interrupt pin, an edge-detecting interrupt pin (rising edge, falling edge, or both), or a general-purpose I/O pin.



Figure 1-1 EPORT Block Diagram

Features

- Rising/falling edge select
- Low-level sensitive
- > Ability for software generation of external interrupt event
- Interrupt pins configurable as general-purpose I/O

To obtain more information about the EPORT or other C^*Core^{TM} products, please contact the C^*Core Technology Co., Ltd. by phone: 0512-68091377, email: <u>support@china-core.com</u> or web: <u>http://www.china-core.com</u>. C^*Core^{TM} is a trade mark of C^*Core Co., Ltd.