PIT Module

Summary

The PIT is a 16-bit timer that provides precise interrupts at regular intervals with minimal processor intervention. The timer can either count down from the value written in the PIT modulus register, or it can be a free-running down-counter.



PIT Block Diagram

Features

- > 16-bit counter with modulus "initial count" register
- Selectable as free running or count down
- ▶ 16 selectable prescalers 2^0 to 2^{15}

To obtain more information about the PIT or other C^*Core^{TM} products, please contact the C^*Core Technology Co., Ltd. by phone: 0512-68091377, email: <u>support@china-core.com</u> or web: <u>http://www.china-core.com</u>. C^*Core^{TM} is a trade mark of C^*Core Co., Ltd.

Tel/Fax: 0512-68091375/68096251 Email: support@china-core.com C*Core Technology Co. Ltd. reserves the right to make changes without further notice to any products herein. Typical parameters which may be provided in C*Core Technology Co. Ltd. data sheets can and do vary in different application environments and over time and man (first be verified by customer technical experts before systems may be designed using C*Core Technology Co. Ltd. products.